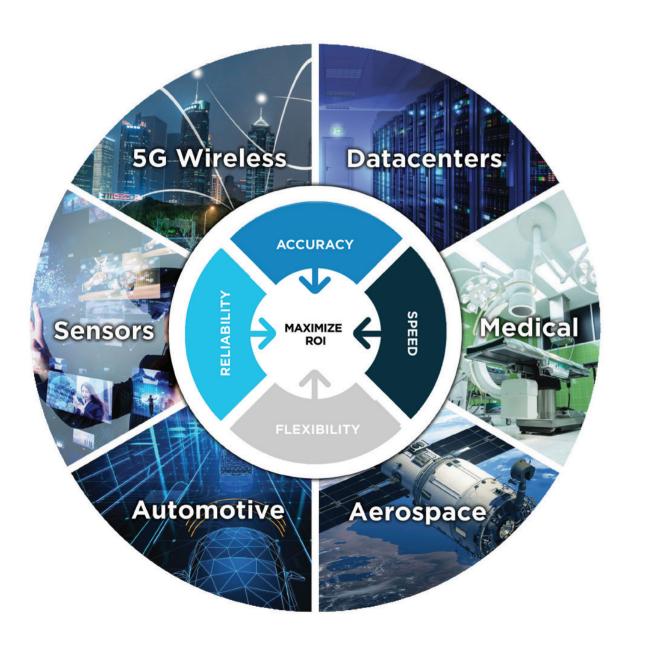
Bringing tomorrow's electronics to life



Bringing tomorrow's electronics to life





554 Clark Rd., Tewksbury, MA



101, Block A, Huahan Innovation Park, Langshan Road, Shenzhen, China 518057 Tel: +86 755 26414155

mrsisystems.com



MRSI-S-HVM **0.5 MICRON DIE BONDER**







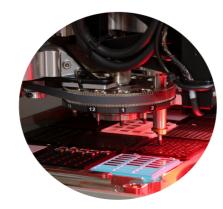
MRSI-S-HVM Applications

Applications & Features

- Designed for integrated photonics volume manufacturing applications, semiconductor wafer level packaging, multi-chips, multi-processes production in one machine.
- Two modes with auto-change over: ±0.5µm @ 3 σ and ±1.5µm @ 3 σ ; both with on-axis z-force for die bonding. MRSI proprietary high z-force option available.
- Capable of Chip-on-Wafer (CoW); Chip-on-Interposer (CoI); Silicon photonics; die from III-V wafer (8 inches) picked & placed onto a silicon wafer (12 inches) and mapping.
- Multiple processes, including DAF, eutectic, epoxy stamping and dispensing, thermal heating from top and bottom, and MRSI proprietary bottom laser soldering.
- Flip chip bonding with direct alignment of fiducials on both bonding interfaces without additional reference or calibration required.
- MRSI proprietary wafer table with automated leveling.
- MRSI-S-HVM inherited all of the MRSI-HVM's parallel processes using MRSI proprietary auto tool change and dual gantry/head.
- Material input methods include wafer, Waffle pack, and Gel-Pak®, as well as customized fixtures.

Value to our Customers

- Industry-leading throughput, superior flexibility, and ultra-high-accuracy in high-volume, high-mix manufacturing, multiple process options.
- Being able to switch between 0.5 micron and 1.5 micron modes allows customers to balance different bonding accuracy requirements in one machine for the best throughput and ROI.
- Easy to use icon-based interface running on a Windows™ platform for easy programming, and low-cost machine maintenance.
- Industry-leading local technical support teams and application expertise.
- 35+ years of experiences in the industry with reliable 24/7 field operations.





MRSI-S-HVM 0.5 Micron Die Bonder

CONFIGURATION	STANDARD	S-HVM-C	S-HVM-L
APPLICATIONS			
0.5µm Placement	•	•	
Chip-on-Carrier (CoC)	• (1)	•	•
Chip-on-Submount (CoS)	•	•	•
Chip-on-Baseplate or Board (CoB)	•	•	•
High Density Top Heating Eutectic Bonding	•	•	•
Wafer Level Packaging	•		•
Silicon Photonics	•	•	•
Co-Packaged Optics	•	•	
Multi-chip, Multi-process Production in One Machine	•	•	•

Note (1) 1.5µm accuracy mode only

PROCESSES			
Eutectic Bonding	•	•	•
Epoxy Stamping	•	•	•
UV Epoxy Dispensing	•	•	•
Localized Heating	•	•	•
Thermal Compression Bonding	•	•	•
Flip-chip Bonding	•	•	•
Co-planarity Bonding	•	•	•

FEATURES & OPTIONS			
Dual Gantry/Head	•	•	•
0.5µm Alignment Systems	•	•	
Laser Soldering	•		•
Heated Head (R)*	•	•	•
On-the-fly Tool Change (L)	•	•	•
Remote Auto Tool Change (R)	•	•	•
Output Stage (R)		•	
8"/12" Wafer Table for CoW (R)	•		•
Input GP/WP & Wafer	•	•	•
Epoxy Dispensing and Stamping	•	•	•

^{*}R=Right Side, L=Left Side